Amendment and Response under 37 C.F.R. 1.116
Applicant: Kenneth Kay Smith et al.
Serial No.: 10/790,360
Filed: March 1, 2004
Docket No.: 10014266-1
Title: SYSTEM FOR ERROR CORRECTION CODING AND DECODING

IN THE CLAIMS

Please cancel claims 11-12 and 14.

Please amend claims 1-4, 8, 10, 13, 15, 23, and 26 as follows:

1.   (Currently Amended) A~~n~~ error correction system for ~~error correction~~ coding and decoding at least one information block, comprising:

first and second encoders each configured to encode the at least one information block, ~~wherein the second encoder has a higher capability than the first encoder and~~

wherein the first encoder generates first parity symbols during encoding of the at least one information block, and

wherein the second encoder generates second parity symbols during encoding of the at least one information block, with a quantity~~number~~ of the second parity symbols being greater than a quantity~~number~~ of the first parity symbols; and

first and second decoders configured to recover the at least one information block,

wherein the first decoder and the second decoder are configured to operate sequentially in time with the first decoder acting first and the second decoder acting second,

wherein the first decoder is configured to recover the at least one information block using the first parity symbols~~generated when the first encoder encodes the information, and provide an indication if the information cannot be recovered~~, and

wherein the second decoder is configured to recover~~s~~ the at least one information block ~~encoded by the second encoder~~ via the second parity symbols with the second decoder configured to remain inactive during operation of the first decoder and further remain inactive after operation of the first decoder unless ~~only if~~ the first decoder is unsuccessful in attempting to ~~cannot~~ recover the at least one information block and until the first decoder provides an indication to the second decoder to recover the at least one information block ~~with the second decoder configured to recover the information using the second parity symbols generated when the second encoder encodes the information.~~

Amendment and Response under 37 C.F.R. 1.116
Applicant: Kenneth Kay Smith et al.
Serial No.: 10/790,360
Filed: March 1, 2004
Docket No.: 10014266-1
Title: SYSTEM FOR ERROR CORRECTION CODING AND DECODING

2.   (Currently Amended) The system of claim 1, wherein the <u>at least one </u>information <u>block</u> is encoded to produce a codeword by combining the information, the first parity symbols, and the second parity symbols.

3.   (Currently Amended) The system of claim 2, comprising:
        a multiplexer configured to combine the<u> at least one </u>information<u> block,</u> the first parity symbols and the second parity symbols into the codeword.

4.   (Currently Amended) The system of claim~~1~~<u>3</u>, wherein the<u> at least one </u>information <u>block</u> is encoded by combining the<u> at least one </u>information<u> block,</u> the first parity symbols, and the second parity symbols generated when the second encoder encodes both the<u> at least one</u> information <u>block</u> and the first parity symbols.

5-7.   (Canceled)

8.   (Currently Amended) The system of claim 4, wherein the second decoder is configured to recover the<u> at least one </u>information<u> block</u> by using second parity symbols generated when the second encoder encodes both the <u>at least one </u>information <u>block </u>and the first parity symbols.

9.   (Canceled)

10.   (Currently Amended) The system of claim 1, wherein the second decoder comprises:
        a register configured to store the<u> at least one</u> information <u>block </u>and the second parity symbols; and
        a processor system configured to recover the<u> at least one </u>information<u> block</u> by using second parity symbols only if the first decoder provides the indication.

11-12. (Canceled)

Amendment and Response under 37 C.F.R. 1.116
Applicant: Kenneth Kay Smith et al.
Serial No.: 10/790,360
Filed: March 1, 2004
Docket No.: 10014266-1
Title: SYSTEM FOR ERROR CORRECTION CODING AND DECODING

13. (Currently Amended) A storage system having an error correction system for ~~error correction~~ coding and decoding at least one information block, comprising:

a first encoder ~~and second encoders each~~ configured to encode the at least one information block~~, wherein the first encoder~~ using ~~applies~~ first parity symbols;

a ~~and the~~ second encoder configured to encode the at least one information block by applying~~ies~~ second parity symbols which are greater in number than the first parity symbols to enable locating and correcting a larger number of errors than the first encoder; ~~and~~

a first decoder ~~and second decoders~~ configured to recover the at least one information block~~, wherein the first decoder acts first to recover the information~~; and

a~~the~~ second decoder configured~~acts second~~ to recover the at least one information block sequentially in time after operation of the first decoder, wherein ~~encoded by the second encoder~~ the second decoder initiates action to recover the at least information block only after completion by ~~if~~ the first decoder of an unsuccessful attempt ~~cannot~~ to recover the at least one information block, and further wherein the first and second encoders and the first and second decoders use an error correcting code;

wherein the first decoder and the second decoder are configured to recover the at least one information block via separating the at least one information block from the first parity symbols and the second parity symbols, respectively.

14. (Canceled)

15. (Currently Amended) The system of claim 14, wherein the first and second encoders and the first and second decoders ~~the linear block code is~~use a cyclic redundancy check code.

16. (Cancelled)

17. (Original) The system of claim 13, wherein the first and second encoders and the first and second decoders use a burst-correcting code.

18-22. (Canceled)

Amendment and Response under 37 C.F.R. 1.116
Applicant: Kenneth Kay Smith et al.
Serial No.: 10/790,360
Filed: March 1, 2004
Docket No.: 10014266-1
Title: SYSTEM FOR ERROR CORRECTION CODING AND DECODING

23.   (Currently Amended) A method of error correction coding and decoding information, comprising:

generating a number of first parity symbols from the information;

generating a number of second parity symbols from the information, wherein the number of the second parity symbols is greater than the number of the first parity symbols;

combining the first parity symbols, the second parity symbols and the information into an encoded data block;

recovering the information from the encoded data block, if no errors are present in the encoded data block, via disassembling the encoded data block by removing both the first parity symbols and the second parity symbols from the information;

recovering the information from the encoded data block, if an error is present in the encoded data block, by first using the first parity symbols if the information ~~can~~is capable of being~~be~~ recovered using the first parity symbols; and

recovering the information from the encoded data block, after using the first parity symbols, by second using the second parity symbols only if recovery of the information was not accomplished ~~cannot be recovered~~by using the first parity symbols.

24.   (Original) The method of claim 23, wherein generating a number of the second parity symbols from the information includes generating the number of the second parity symbols from both the first parity symbols and the information.

25.   (Original) The method of claim 23, wherein recovering the information from the encoded data block using the first parity symbols includes detecting or correcting errors in the information which can be detected or corrected using the first parity symbols.

26.   (Currently Amended) The method of claim 23, wherein recovering the information from the encoded data block using the second parity symbols includes detecting or correcting errors in the information which can be detected or corrected using the second parity symbols.

Amendment and Response under 37 C.F.R. 1.116
Applicant: Kenneth Kay Smith et al.
Serial No.: 10/790,360
Filed: March 1, 2004
Docket No.: 10014266-1
Title: SYSTEM FOR ERROR CORRECTION CODING AND DECODING

27. (Original) The method of claim 23, wherein recovering the information from the encoded data block using the second parity symbols includes detecting or correcting errors in the first parity symbols and the information using the second parity symbols.